Basic Information :

| **Name :** | Ahmed ~ Saeed |
| **Title :** | Assistant Professor |

Ahmed Saeed obtained his BSc and MSc in Communications and Electronics Engineering from Faculty of Engineering, Helwan University, Egypt in 2004 and 2010, respectively, where he was the second of his class. He obtained his PhD in Electrical Engineering "Electronics and Communication" from Ainshams University. Ahmed was a visiting researcher at System and Architecture Lab in Faculty of Engineering, University of Porto, Portugal in 2013/2014. From 2004 to 2016 he was Teaching Assistant in the Electrical Engineering department. Currently, he is Assistant professor in Electrical Engineering Department, Future University in Egypt. in April 2016, Ahmed was visiting staff at Faculty of Engineering, University of Porto, Portugal. He is also a Trainer, Editor,and Digital system designer. Ahmed Assisted and supervised a lot of graduation/commercial projects in the field of Signal Processing, communication systems implementation, and digital system design. He is member in the organization/steering committee for the scientific meetings and events held in Faculty of Engineering. His research interests include low-power implementation of signal processing functions and wired/wireless communication systems on FPGA and ASIC, signal analysis and modelling in high-speed interconnects and Signal Integrity. Ahmed is member in IEEE and Egyptian Syndicate of Engineers. He published some papers in efficient implementation of the DSP blocks on an FPGA and Signal Integrity.

Education :

Certificate	Major	University	Year
PhD	Electronics Engineering	Ain Shams University- Faculty Of Engineering	2017
Masters	Electronics Engineering	Helwan University - Faculty Of Engineering	2010
Bachelor	Electronics and Communication Engineering	Helwan - Egypt	2004

Teaching Experience :

Name Of Organization	Position	From Date	To Date
FUE	Assistant Professor	01/01/2017	01/01/2017
Departamento de Engenharia Eletrotécnica e de Computadores / Faculdade de Engenharia, Universidade do Porto	Visiting Staff	01/01/2016	01/01/2016
Many Training Company	trainer	01/01/2005	01/01/2012
Modern University	Teaching Assistant	01/01/2005	01/01/2007

Research :

Efficient FPGA implementation of FFT/IFFT Processor
FPGA implementation of Radix-22 Pipelined FFT Processor
Implementation of Fast Discrete Wavelet Transform for Vibration Analysis on an FPGA

Conference :

a
3rd WSEAS international symposium on Wavelets theory and applications in applied mathematics, signal processing & modern science

http://www.fue.edu.eg

26th National Radio Science Conference
the first NTRA knowledge dissemination and networking conference
WINC Conference '08
8th IEEE, IET International Symposium on COMMUNICATION SYSTEMS, NETWORKS AND DIGITAL SIGNAL PROCESSING
Clock signal characterization for signal integrity

Awards :

Award	Donor	Date
Outstanding Assistant Lecturer Award	Future University in Egypt	01/01/2016
Academic Staff, MOBILE + project, in the framework of the European Programme Erasmus+	Departamento de Engenharia Eletrotécnica e de Computadores / Faculdade de Engenharia. Universidade do Porto	01/01/2016
PhD Mobility	Universidade do Porto	01/01/2014
FUE Award to encourage scientific research	Future University in Egypt	01/01/2011

http://www.fue.edu.eg