Basic Information :

Name :	AHMED SAEED ABDELSAMEA SAYED
Title :	Assistant Professor

Ahmed Saeed obtained his PhD in Electrical Engineering "Electronics and Communication" from Ainshams University and, during his work toward PhD, he was a visiting researcher at System and Architecture Lab in Faculty of Engineering, University of Porto, Portugal. He obtained his BSc and MSc in Communications and Electronics Engineering from Faculty of Engineering, Helwan University, Egypt in 2004 and 2010, respectively, where he was the second of his class. From 2004 to 2016 he was Teaching Assistant in the Electrical Engineering department. Currently, he is an assistant professor in Electrical Engineering Department, Future University in Egypt. In April 2016, Ahmed was visiting staff at Faculty of Engineering, University of Porto, Portugal. In 2018, Ahmed was visiting staff at the School of Engineering, University of Central Lancashire, United Kingdom. He is also a Trainer, Editor,and Digital system designer. Ahmed Assisted and supervised a lot of graduation/commercial projects in the field of IoT, signal processing, communication systems implementation, and digital system design. He is a member in the organization/steering committee for the scientific meetings and events held in Faculty of Engineering. His research interests include low-power implementation of signal processing functions and wired/wireless communication systems on FPGA and ASIC, signal analysis and modeling in high-speed interconnects and Signal Integrity. Ahmed is a member in IEEE and Egyptian Syndicate of Engineers. He published some papers in efficient implementation of the DSP blocks on an FPGA and signal integrity.

Education:

Certificate	Major	University	Year
PhD			2017
Masters	Electronics Engineering		2010
Bachelor			2004

Teaching Experience:

Name Of Organization	Position	From Date	To Date
FUE	Teaching Staff Member	09/03/2008	Current
University of Central Lancashire	Visiting Staff	01/01/2018	01/01/2018
FUE	Assistant Professor	01/01/2017	01/01/2017
Departamento de Engenharia Eletrot®cnica e de Computadores / Faculdade de Engenharia, Universidade do Porto	Visiting Staff	01/01/2016	01/01/2016
Modern University	Teaching Assistant	01/01/2005	01/01/2007
Many Training Company	trainer	01/01/2005	01/01/2012

Researches / Publications :

Vandermonde-Interpolation Method with Chebyshev Nodes for Solving Volterra Integral Equations of the Second Kind with Weakly Singular Kernels
Simulation of High open-circuit voltage Perovskite/CIGS-GeTe tandem cell
Efficient self-protected thin film c-Si solar cell against reverse-biasing condition: A simulation study
Investigation of Electron Transport Material-Free Perovskite/CIGS Tandem Solar Cell
Analysis of an Efficient ZnO/GeTe Solar Cell Using SCAPS-1D
The Interpolation-Vandermonde Method for Numerical Solutions of Weakly Singular Volterra Integral Equations of the Second Kind

High-Efficiency Electron Transport Layer-Free Perovskite/GeTe Tandem Solar Cell: Numerical Simulation
Interpolation method for solving Volterra integral equations with weakly singular kernel using an advanced barycentric Lagrange formula
High-efficiency modified tandem solar cell: Simulation of two-absorbers bottom subcell
Two-Terminal Perovskite/Silicon Solar Cell: Simulation and Analysis
Simulation of High-Efficiency Perovskite-Based Tandem Solar Cells
Thirteen-Level Modified Packed U-Cell Multilevel Inverter for Renewable-Energy Applications
High Efficiency Tandem Perovskite/CIGS Solar Cell
A Comparative Study Between Modified MPPT Algorithms Using Different Types of Solar Cells
A comprehensive simulation study of hybrid halide perovskite solar cell with copper oxide as HTM
Implementation of a Low-power Embedded Processor for IoT Applications and Wearables
Implementation of Low-Power Multiply-Accumulate (MAC) Unit for IoT Processors
Quantitative Characterization of Clock Signals in the Frequency Domain for Signal Integrity Analysis
Clock signal characterization for signal integrity
Implementation of Fast Discrete Wavelet Transform for Vibration Analysis on an FPGA
the first NTRA knowledge dissemination and networking conference
Efficient FPGA implementation of FFT/IFFT Processor
FPGA implementation of Radix-22 Pipelined FFT Processor
3rd WSEAS international symposium on Wavelets theory and applications in applied mathematics, signal processing & modern science
26th National Radio Science Conference

Awards:

Award	Donor	Date
IEEE XploreÈ Challenge for Researchers	IEEE	01/01/2018
Outstanding Assistant Lecturer Award	Future University in Egypt	01/01/2016
Academic Staff, MOBILE + project, in the framework of the European Programme Erasmus+	Departamento de Engenharia Eletrot®cnica e de Computadores / Faculdade de Engenharia. Universidade do Porto	01/01/2016
PhD Mobility	Universidade do Porto	01/01/2014
FUE Award to encourage scientific research	Future University in Egypt	01/01/2011