Basic Information :

Name :	Mohamed Ahmed Ibrahim Mead
Title :	part-time Assistant Lecturer

Mohamed Mead is a part-time Assistant Lecturer of Administrative information systems at Faculty of Economics and Political Science, Future University in Egypt. He is working as lecturer at the faculty of Computer Sciences and Information System, Suez Canal University. He worked as a lecturer in the Higher Institute of Computer Sciences at Information System at October 6 University from January 2012 to December 2012.He worked also as lecturer in the Technical College of Tabuk in Saudi Arabia from February 2002 to July 2010.
He has many publications in the field of Computer science. He holds his PhD in Computer Science from the Department of Mathematics at Faculty of Science at Cairo University in 2012. He received his M.Sc. in computer science from the department of Mathematics at Faculty of Science at Cairo University in 2002, and B.Sc. in Statistics and Computer Science, Mathematics Department, Faculty of Science, Ain Shams University in 1996.

Education :

Certificate	Major	University	Year
PhD	Computer Science, Mathematics Dept	Faculty of Science, Cairo University	2012
Masters	Computer Science-Mathematics Dept	Faculty of Science, Cairo University	2002
Bachelor	Statistics & Computer Science	Faculty of Science, Ain Shams University	1996

Teaching Experience :

Name Of Organization	Position	From Date	To Date
Faculty of Computer Science & Information System, Suez Canal University	Lecturer	01/01/2013	22/05/2014
Higher Institute of Computer Science & Information System, 6th of Oct City	Lecturer	01/01/2012	01/12/2012
Technical College of Tabuk Saudi Arabia	Lecturer	01/02/2002	01/07/2010
Professional	Web Developer	01/10/2000	01/01/2002
Arab Net Company	Web Developer	01/10/1999	01/10/2000
Computer & System Lap, ElHegas Institute for Specific Studies	Instructor	01/10/1998	01/06/1999
International Office for Trade	Programmer	01/01/1997	01/01/1998

Research :

Automatic loop transformation selection with the aid of kohonen’s self-organizing maps for parallelizing compilers (English),PDPTA, 850-856

Developing an Intelligent Layer for Automatic Parallel Detection Implemented on Different High Performance Computing Platform), PDPTA, 850-856